

## Chapter 4 8085 Microprocessor Architecture And Memory

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Microprocessor & Interfacing (140701) Rahul Patel 1 Chapter 4 8085 Microprocessor Architecture and Memory Interfacing by Rahul Patel, Assistant Professor, EC Dept., Sankalchand Patel College of Engg., Visnagar

Chapter 4 8085 Microprocessor Architecture and Memory ...

3 Chapter 4: 8085 Microprocessor Architecture and Memory Interfacing 5 – During the execution of the instruction, these lines carry the low-order address bits during the early part of the cycle, then during the later part of the cycle, they carry the 8 data bits. – This is known as multiplexing the bus.

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Chapter 4: 8085 Microprocessor Architecture and Memory Interfacing 28 Register Array • The programmable registers were discussed in the last chapter • Two additional registers W and Z are included in the register array.

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4 Chapter 4: 8085 Microprocessor Architecture and Memory Interfacing 7 The Control and Status Signals • IO/M: This is a status signal used to differentiate between I/O and memory operations. – When it is high (IO/M=1), it indicates an I/O operation. – When it is low (IO/M=0), it indicates a memory operation.

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Chapter 4 8085 Microprocessor Architecture And Memory

Chapter 5 The 8085 Microprocessor . 5.1 Architecture of 8085 Microprocessor . 5.1.1 Register Section . 5.1.2 Address Buffer and Address-Data Buffer . 5.1.3 Arithmetic and Logical Unit (ALU)

(PDF) An Introduction to Microprocessor 8085

• The microprocessor reads one instruction at a time, matches it with its instruction set, and performs the data manipulation specified. • The result is either stored back into memory or displayed on an output device. 3. THE 8085 ARCHITECTURE • The 8085 uses three separate busses to perform its operations • The address bus. • The data bus. • The control bus. 4.

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After the request has been processed, the control shifts back to the main program. This interrupt might be a hardware or a software interrupt. Some interrupts can be ignored (or maskable) while some cannot (non-maskable). There are five interrupt signals in 8085 microprocessor: INTR, RST 7.5, RST 6.5, RST 5.5, TRAP. Increment or Decrement Register

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Title: Chapter 3 The 8085 Microprocessor Architecture 1 Chapter 3 The 8085 Microprocessor Architecture. 2 The 8085 and Its Busses. The 8085 is an 8-bit general purpose microprocessor that can address 64K Byte of memory. It has 40 pins and requires 5V power supply. It can run at a frequency of 3 MHz(or 5 MHz). The pins on the chip can be grouped ...

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Microprocessor 8086 MCQ Questions This section focuses on "8086 Microprocessor". These Multiple Choice Questions (MCQ) should be practiced to improve the Microprocessor skills required for various interviews (campus interview, walk-in interview, company interview), placements, entrance exams and other competitive examinations.

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